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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Applicant: Haowen Bu, et al.

Docket Number: TI-36637

Serial No.: **10/810,905**

Art Unit: 2823

Filed: 03/26/04

Examiner: Jarrett J. Stark

For: Improved CMOS Transistors and Methods of Forming
Same

CERTIFICATION OF FACSIMILE TRANSMISSION

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NAME OF INVENTOR(S): Haowen Bu, et al.	
RECEIPT DATE & SERIAL NO.: Serial No.: 10/810,905	
TITLE OF INVENTION: Improved CMOS Transistors and Methods of Forming Same Filing Date: 03/26/04	
TI FILE NO.: TI-36637	DEPOSIT ACCT. NO.: 20-0668
FAXED: 3-8-06 DUE: 4-1-06	
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Texas Instruments Incorporated
PO Box 655474, M/S 3999
Dallas, TX 75265-5474

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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

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Date

ELECTION

This election is offered in response to the Examiner's restriction requirement mailed March 1, 2006.

Applicants hereby provisionally elect to pursue Group I with Claims 1-10, without traverse.

Respectfully submitted,

Texas Instruments Incorporated
P.O. Box 655474, M/S 3999
Dallas, TX 75265
(972) 917-4167



Rose Alyssa Keagy
Attorney for Applicants
Registration No. 35,095